

REMARKS

The present *Supplemental Amendment* further supplements the *Supplemental Amendment* filed November 17, 2006, and is submitted further in response to the Official Action dated February 9, 2006.

The Applicant notes with appreciation the consideration of the Information Disclosure Statements filed on May 29, 1998; October 13, 1998; June 2, 1999; September 13, 1999; May 18, 2000; September 6, 2000; November 2, 2000; January 29, 2001; April 9, 2001; July 17, 2001; September 12, 2001; February 28, 2002; October 1, 2002; October 11, 2002; March 5, 2003; and November 14, 2005.

An Information Disclosure Statement was filed October 2, 2006 (received by OIPE October 4, 2006), and consideration of this Information Disclosure Statement is respectfully requested.

A further Information Disclosure Statement is submitted herewith and consideration of this Information Disclosure Statement is respectfully requested.

Claims 1, 3, 5 and 50-120 were pending in the present application prior to the above amendment. New claims 121-158 have been added to recite additional protection to which the Applicant is entitled. Accordingly, claims 1, 3, 5 and 50-158 are now pending in the present application, of which claims 1, 5, 50-83, 121, 125, 129, 133, 137, 139, 142, 148 and 153 are independent. For the reasons of record, all claims are believed to be in condition for allowance. Favorable reconsideration is requested.

New independent claims 121, 125, 129, 133, 137, 139, 142, 148 and 153 recite that a first pixel electrode is provided on an opposite side of a data line to a second pixel electrode. This feature is supported in the present specification, for example, by the configuration shown in Figures 1B, 6A and 6B, where a first pixel electrode (associated with an n-th gate line) is provided on an opposite side of a data line (e.g. Y_m) to a second pixel electrode (associated with an (n+1)-th gate line). The Applicant specifically directs the Examiner's attention to U.S. Patent No. 4,773,737 to Yokono, which was cited by the Examiner on Form PTO-892, which was attached to the Official

Action mailed January 10, 1995, and to JP 02-033031, which was originally submitted in the Information Disclosure Statement filed May 29, 1998, and considered by the Examiner on February 10, 2005, which may be relevant to the feature of an opposite side. Also, a full English translation of JP '031 is submitted herewith.

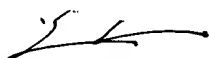
Independent claims 121, 125, 129, and 133 recite a bipolar pulse, which is supported in the present specification, for example, at pages 10 and 11. The Applicant specifically directs the Examiner's attention to U.S. Patent 4,955,697 to Tsukada, which was cited by the Examiner on Form PTO-892, which was attached to the Official Action mailed January 10, 1995, and which may be relevant to the feature of a bipolar pulse.

Independent claims 137 and 139 recite a reverse stagger a-Si TFT and an insulating flattening film, respectively, which are supported in the present specification, for example, by Figure 5.

Independent claims 142, 148 and 152 recite the feature of a bipolar pulse and that a width of a first pulse is different from a width of a second pulse. The latter feature is supported in the present specification, for example, by Figure 3B.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number listed below.

Respectfully submitted,



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